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| ** CONTINUING D | PATA *********************************** | * | | | | • | | | |
| This application is a CON of 10/424,105 04/28/2003 PAT 6,855,642 which is a CON of 09/939,600 08/28/2001 PAT 6,596,650 which is a CON of 09/494,036 01/31/2000 PAT 6,518,201 which is a CON of 09/380,646 09/07/1999 PAT 6,239,041 which is a 371 of PCT/JP98/00892 03/04/1998 | | | | | | | | | |
| ** FOREIGN APPLICATIONS ************************************ | | | | | | | | | |
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| TITLE Method for fabricati | ing semiconductor integrat | ed circui | t device | | | | | | |
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